





Kondo et al.

Examiner:

V. Nguyen

09/852,839

Group Art Unit:

2858

Filed:

5/10/01

Docket:

10873.713US01

Confirmation

5451

Notice of Allow.

Date:

No.: Due Date:

Title:

7/29/03

SEMICONDUCTOR INTEGRATED CIRCUIT WITH NEGATIVE VOLTAGE GENERATION

CIRCUIT, TEST METHOD FOR THE SAME, AND RECORDING DEVICE AND

COMMUNICATION EQUIPMENT HAVING THE SAME

**CERTIFICATE UNDER 37 CFR 1.8:** 

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail, with sufficient postage, in an envelope addressed to: Mail Stop Non-Fee Amendment Commissioner for Patents, P.O. Be 1450, Alexandria, VA 22313-1450 on July 2003.

Mail Stop Non-Fee Amendment Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Sir:

We are transmitting herewith the attached:

Transmittal Sheet in duplicate containing Certificate of Mailing

Amendment and Response

Return postcard

Please consider this a PETITION FOR EXTENSION OF TIME for a sufficient number of months to enter these papers or any future reply, if appropriate. Please charge any additional fees or credit overpayment to Deposit Account No. 13-2725. A duplicate of this sheet is enclosed.

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PATENT TRADEMARK OFFICE

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TECHNOLOGY CENTER 2800